

## REMARKS/ARGUMENTS

### **1. Rejection of claims 1-2 as being unpatentable over Okuzono et al. (US 2001/0043178):**

5      **Response:**

Claim 1:

Claim 1 has been amended to overcome this rejection. Specifically, the limitation “determining a location in the panel for forming the timing control circuit so as to make differences among delay time intervals of the timing signals transmitted to  
10      different logic circuits less than 1000  $\mu$ s, and forming the timing control circuit accordingly” is added in claim 1. This limitation is supported in the specification in [0020] and illustrated in Fig. 3 and Fig. 5, for instance, and no new matter is introduced. Acceptance of this amendment is politely requested.

15      Regarding US 2001/0043178, Okuzono discloses a liquid crystal display. In Okuzono’s disclosure, he teaches supplying an image data on one scan line and using the time T corresponding with one horizontal **period** 15.6 microseconds as described in [0058] and illustrated in Fig. 3. The time 15.6 microseconds in Okuzono’s teaching is the period of the input signal transmitted to the same driver, but not the delay time  
20      differences among the timing signals transmitted to different logic circuits. On the other hand, the method of claim 1 determines the location of the timing control circuit so as to make the differences among delay time intervals of the timing signals transmitted to **different logic circuits** e.g. the. first data line driving circuit and the second data line driving circuit less than 1000 microseconds. Therefore, claim 1 is  
25      patentably distinct from Okuzono’s teaching, and should be allowed. Reconsideration of claim 1 is politely requested.

Claim 2:

Claim 2 is amended to overcome this rejection. Specifically, the limitation “the  
30      timing signal is transmitted to [[the]] different logic circuits by a plurality of

transmitting lines” is added to claim 2. This limitation is supported in the specification in [0020] and illustrated in Fig. 3 and Fig. 5, for instance, and no new matter is introduced. Acceptance of this amendment is politely requested.

5        Claim 2 teaches the timing signal is transmitted to different logic circuits by a plurality of transmitting lines, and differences between a product of an equivalent resistance value and an equivalent capacitance value of each transmitting line are less than 1000  $\mu$ s. In contrast to claim 2, Okuzono only teaches the period of the input  
10        signal is transmitted to different logic circuits by a plurality of transmitting lines, and differences between a product of an equivalent resistance value and an equivalent capacitance value of **each** transmitting line are less than 1000  $\mu$ s. As a result, claim 2 is patentably distinct from Okuzono’s teaching, and should be allowed. Reconsideration of claim 2 is politely requested.

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**2. Rejection of claims 3-5 as being unpatentable over Okuzono in view of Uehara and in further in view of Kim (US 5,808,596):**

**Response:**

Claim 3:

20        Claim 3 is dependent on claim 1, and should be allowed if claim 1 is found allowable. Reconsideration of claim 3 is politely requested.

Claim 4:

25        Claim 4 teaches the timing signal is respectively transmitted to the first data line driving circuit and to the second data line driving circuit by a first transmitting line and a second transmitting line, differences between a product of an equivalent resistance value and an equivalent capacitance value of the first transmitting line and a product of an equivalent resistance value and an equivalent capacitance value of the second transmitting line being less than 1000  $\mu$ s.

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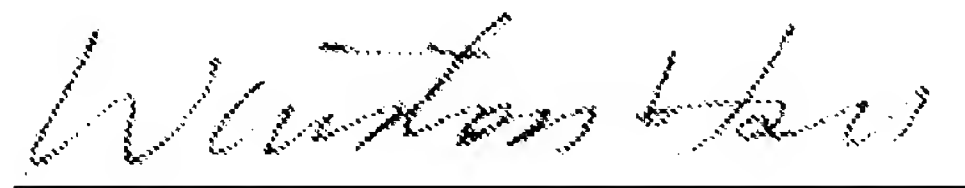
Regarding the cited arts, none of them teaches the differences between a product of an equivalent resistance value and an equivalent capacitance value of **the first transmitting line** and a product of an equivalent resistance value and an equivalent capacitance value of **the second transmitting line** being less than 1000  $\mu$ s. Therefore,  
5 claim 4 is patentably distinct from the cited arts, and reconsideration of claim 4 is politely requested.

Claim 5:

Claim 5 is dependent on claim 1, and should be allowed if claim 1 is found  
10 allowable. Reconsideration of claim 5 is politely requested.

Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

15 Sincerely yours,



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Note: Please leave a message in my voice mail if you need to talk to me. (The time in  
25 D.C. is 13 hours behind the Taiwan time, i.e. 9 AM in D.C. = 10 PM in Taiwan.)